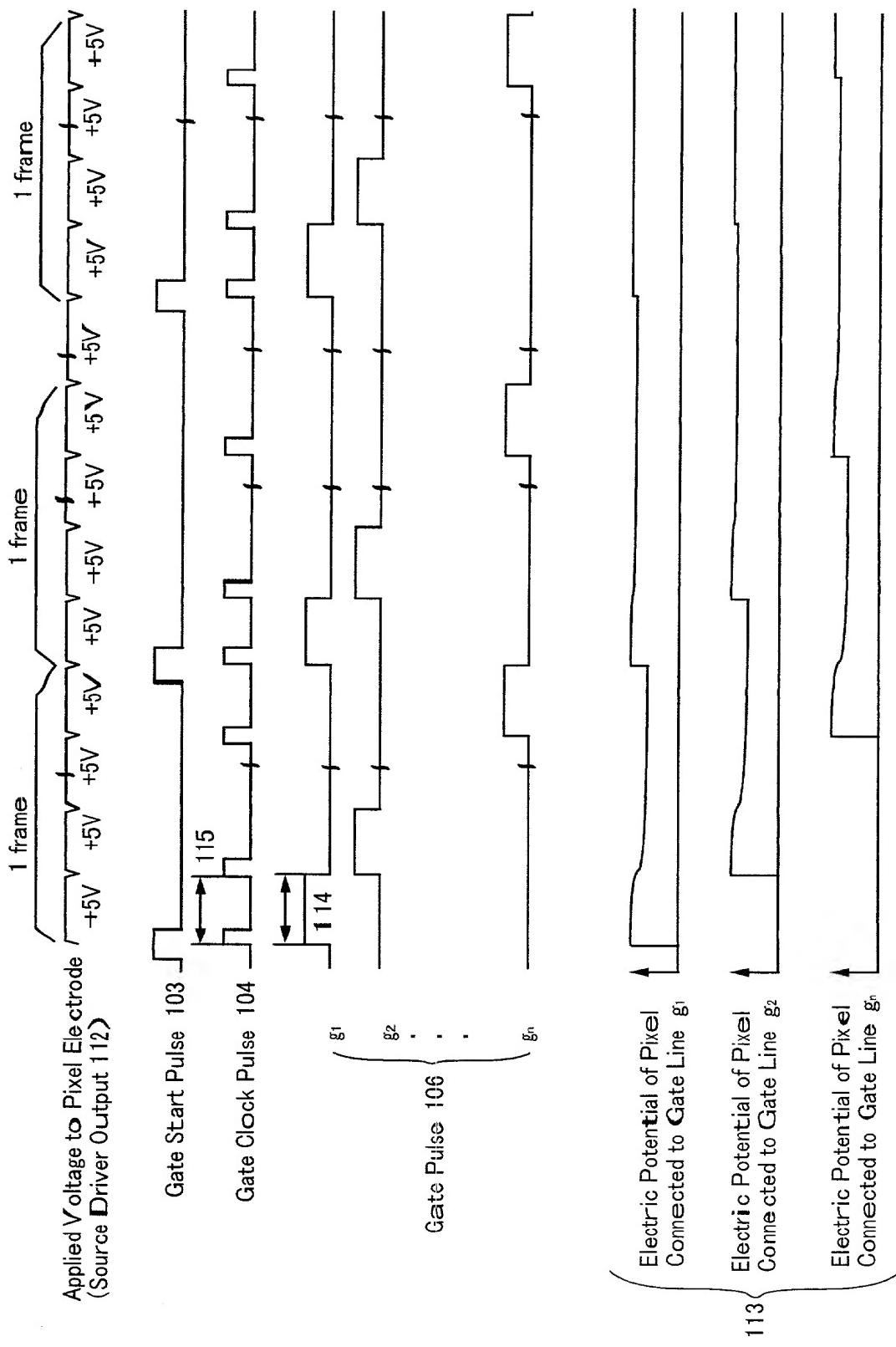


Fig. 1



2

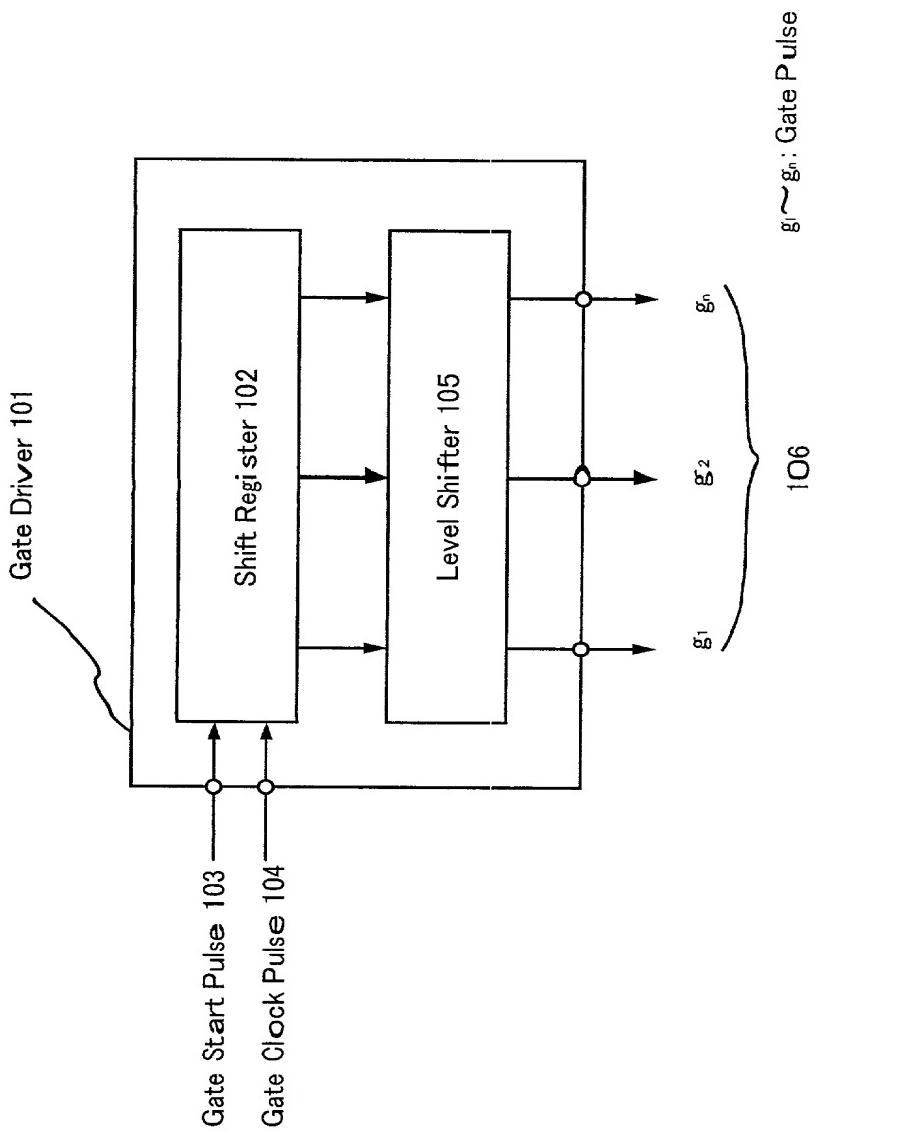


Fig. 3

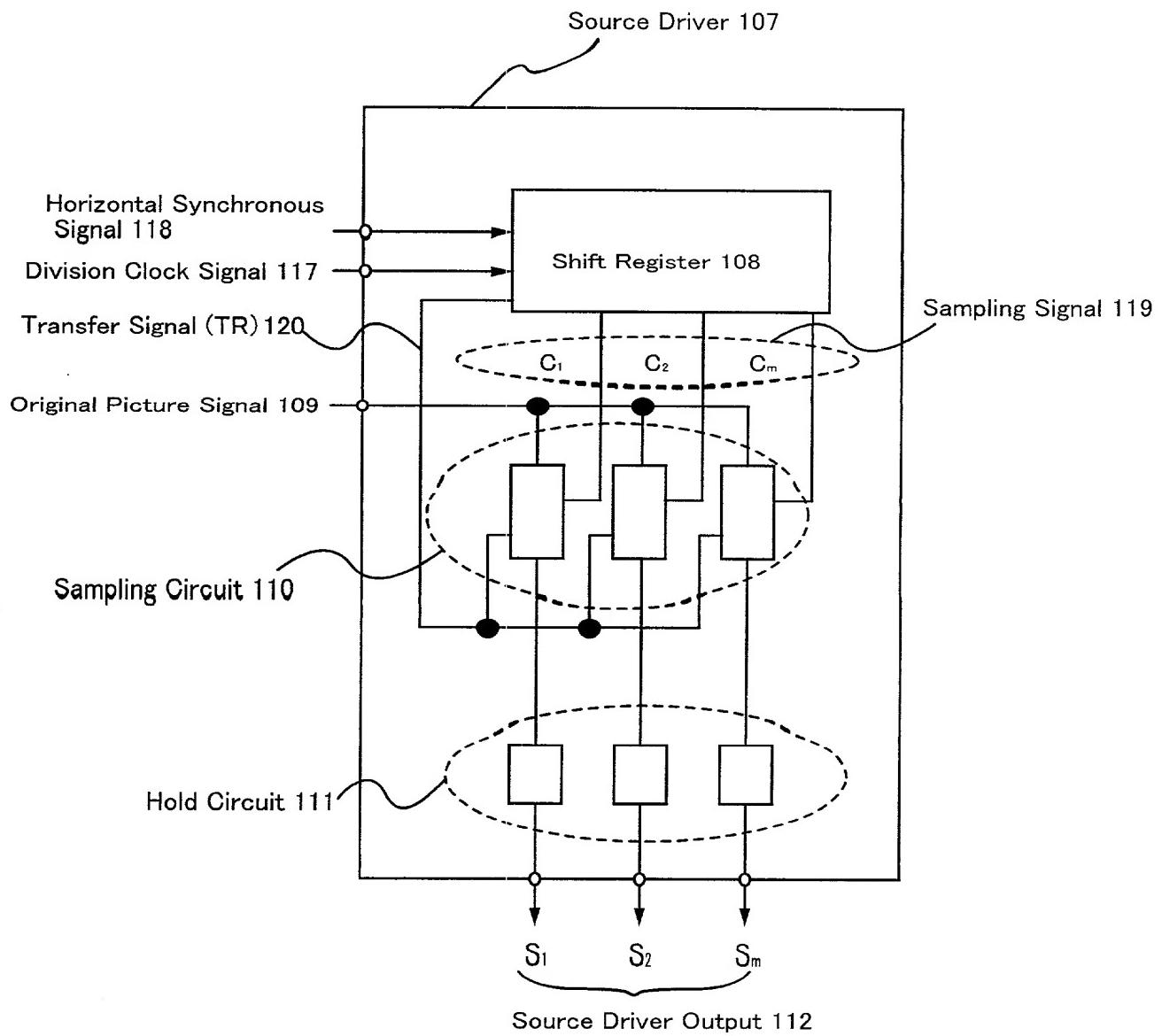


Fig. 4

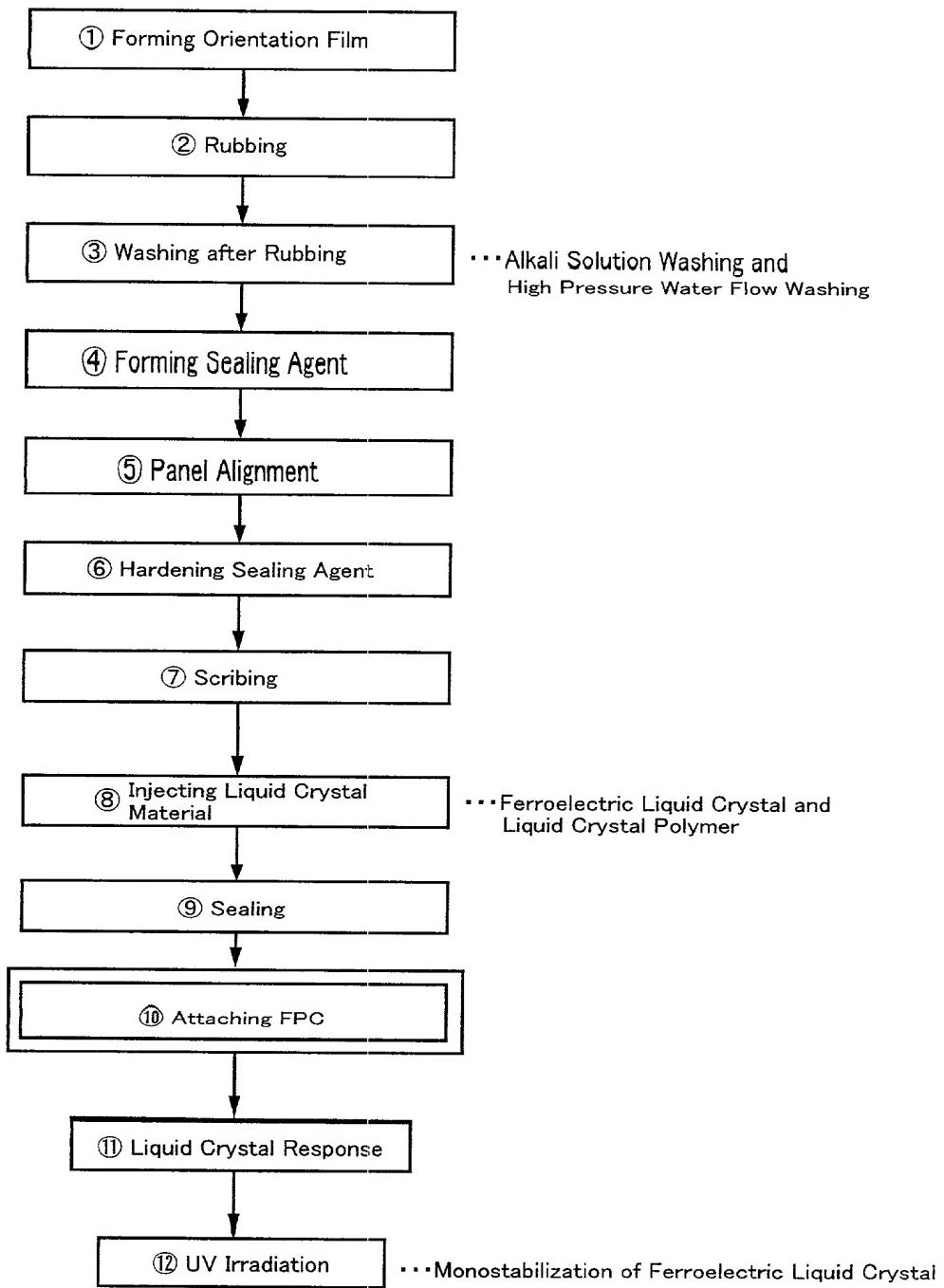


Fig. 5

4,000,000 4,200,000

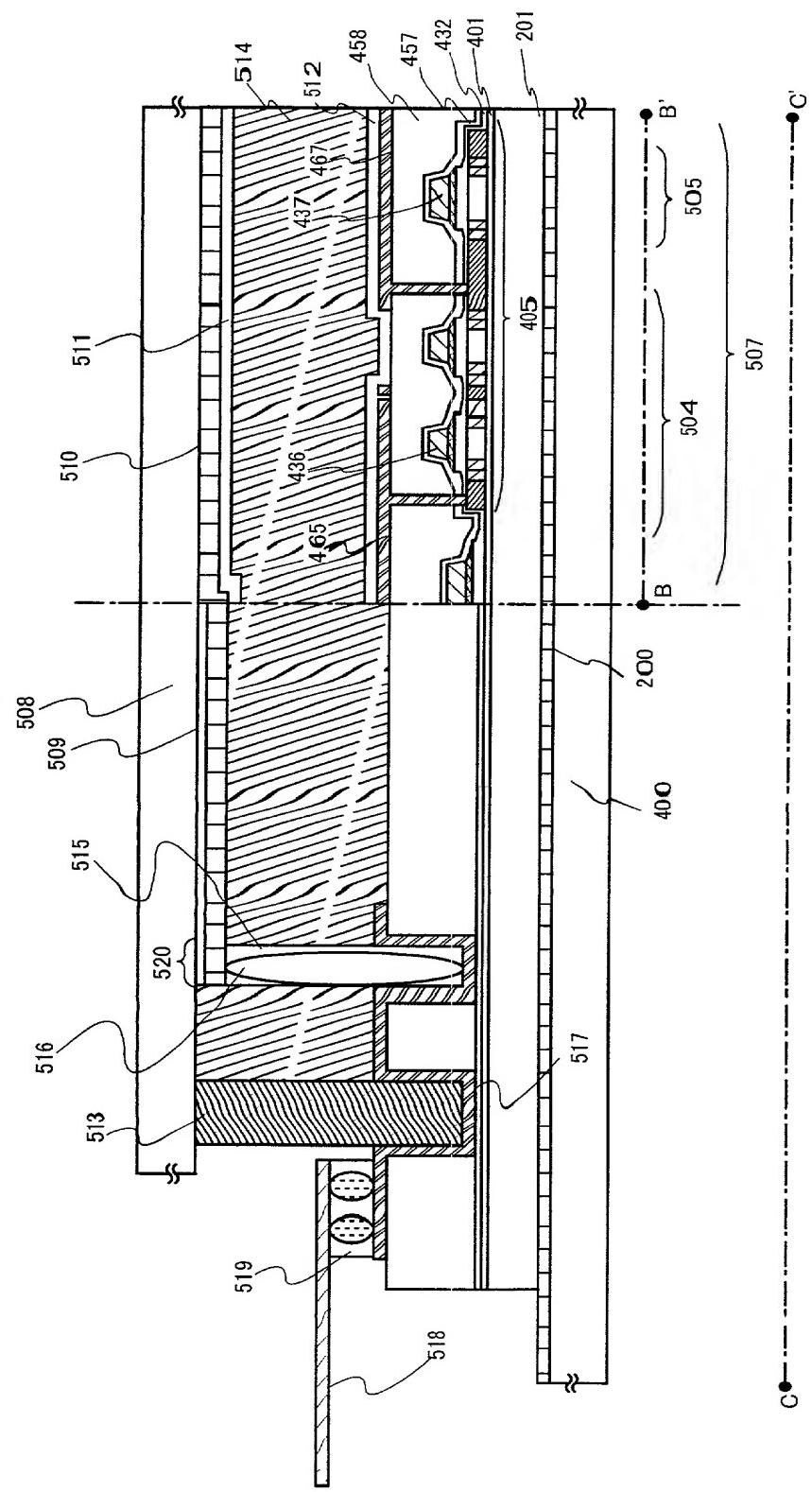


Fig. 6

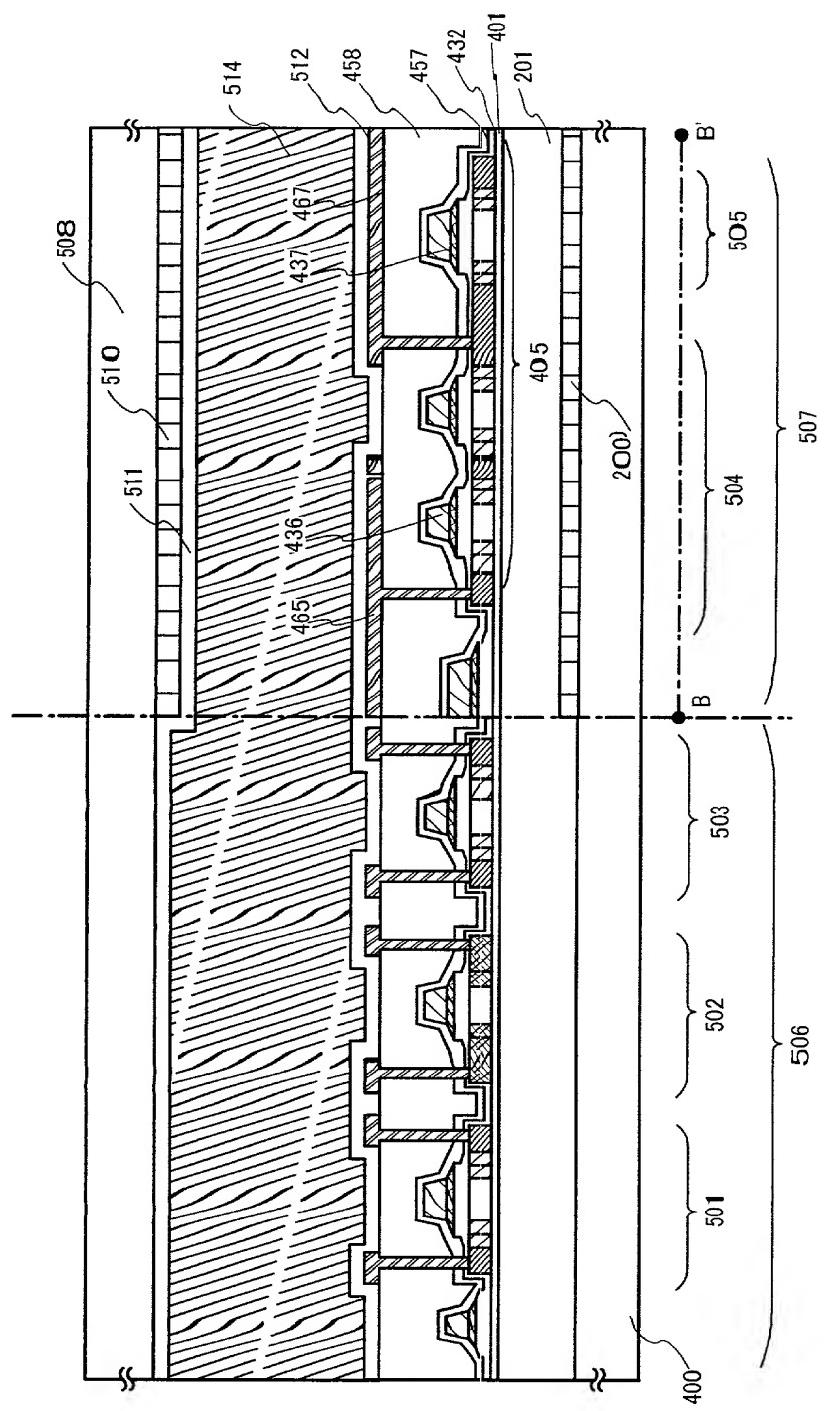


Fig. 7

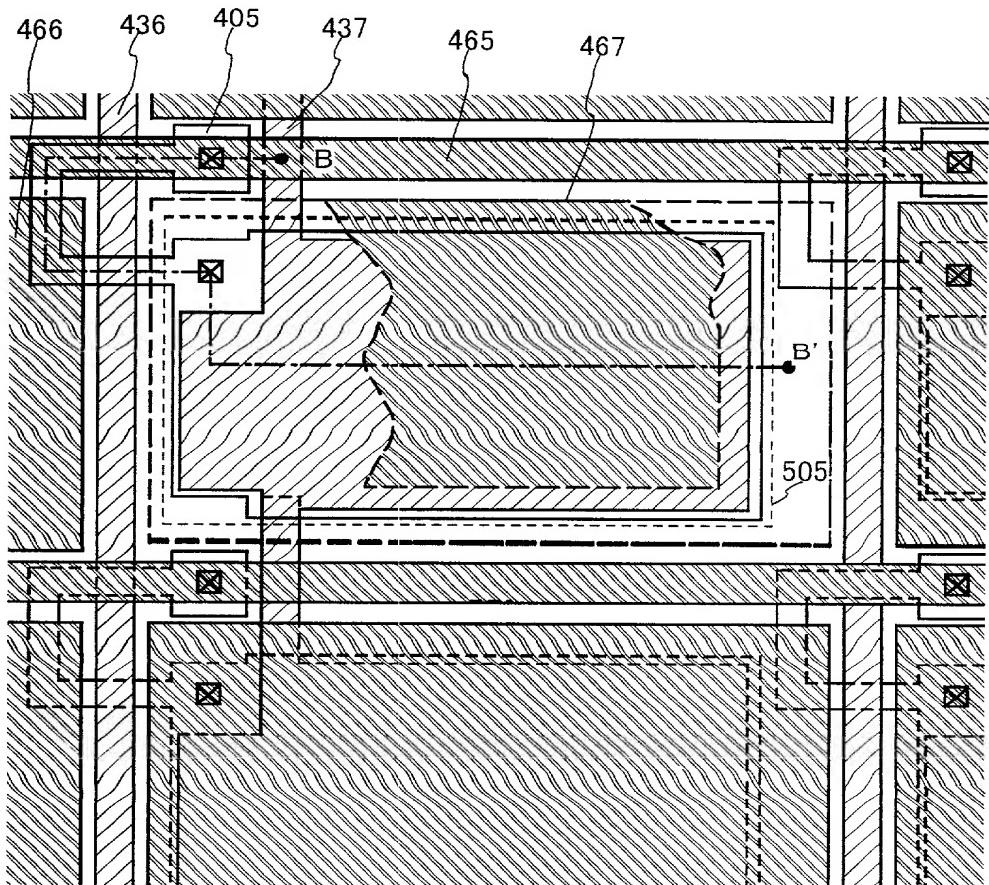


Fig. 8

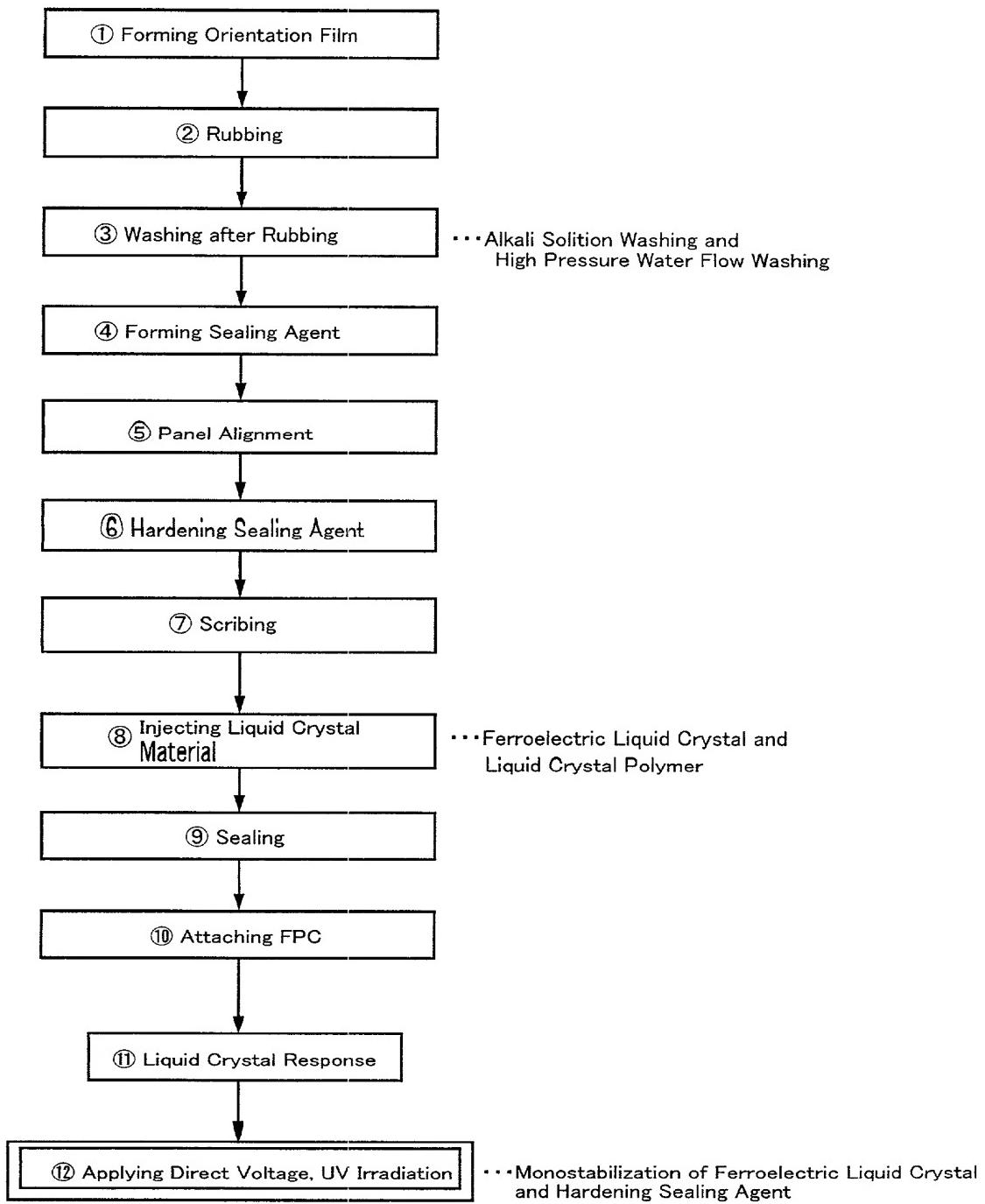


Fig. 9

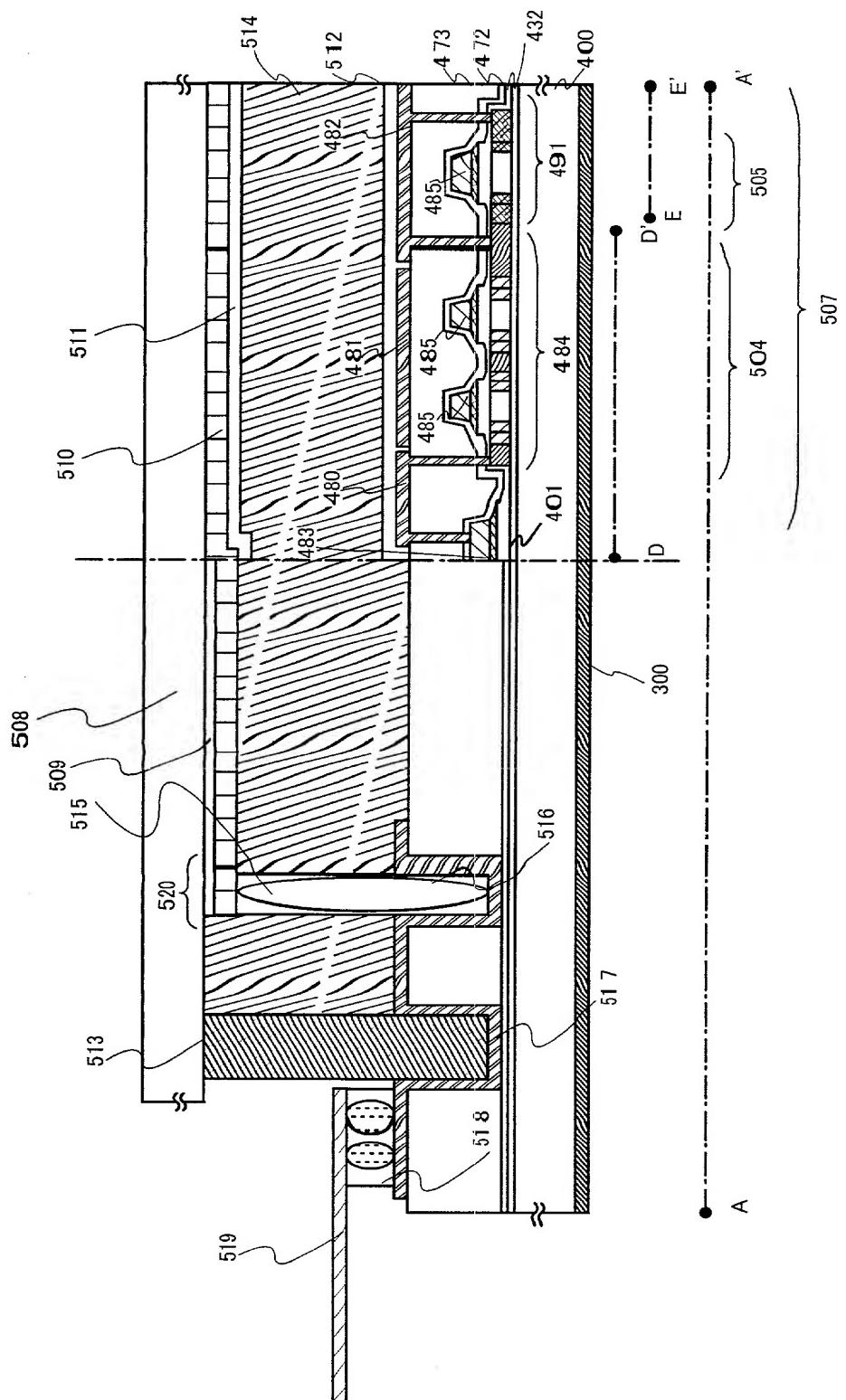


Fig. 10

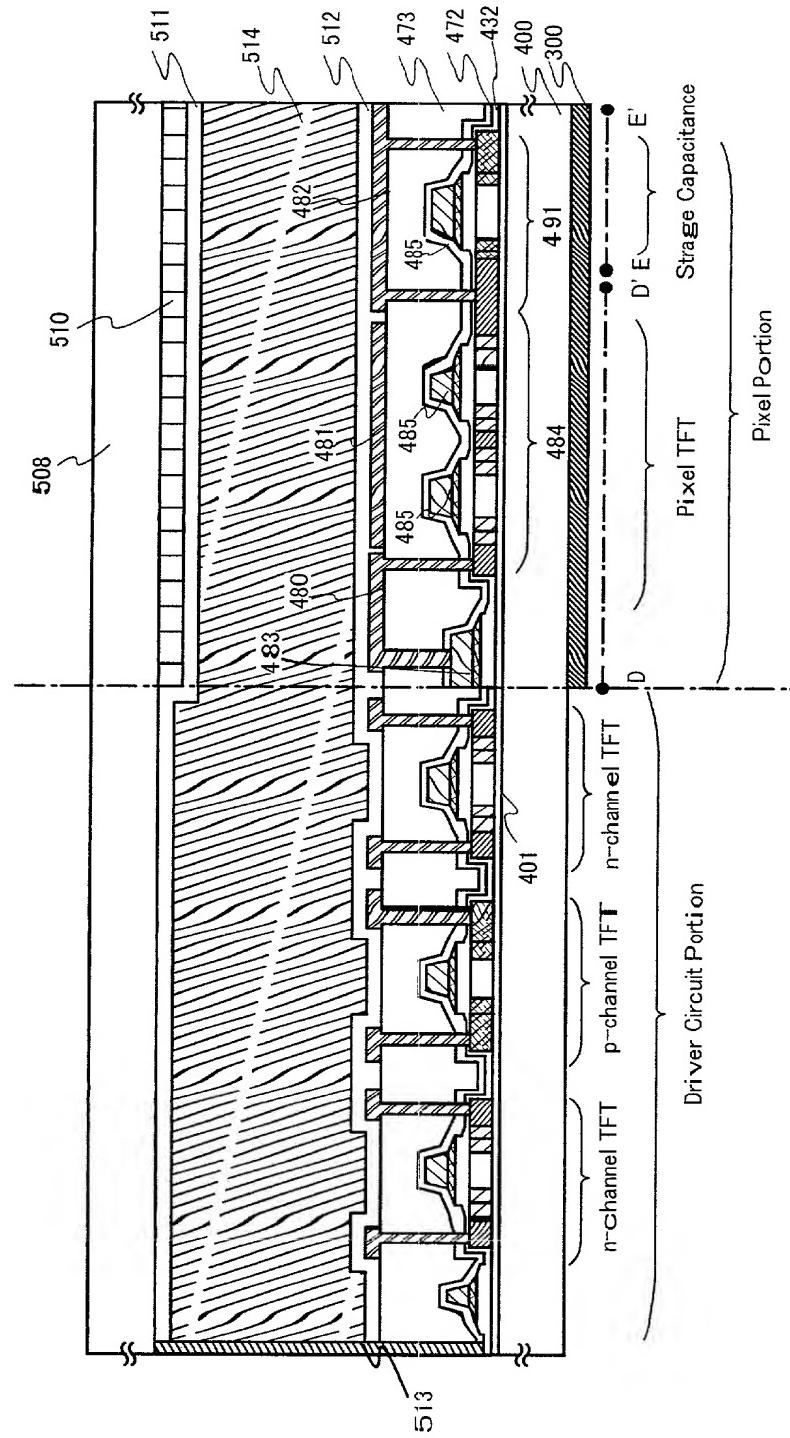


Fig. 11

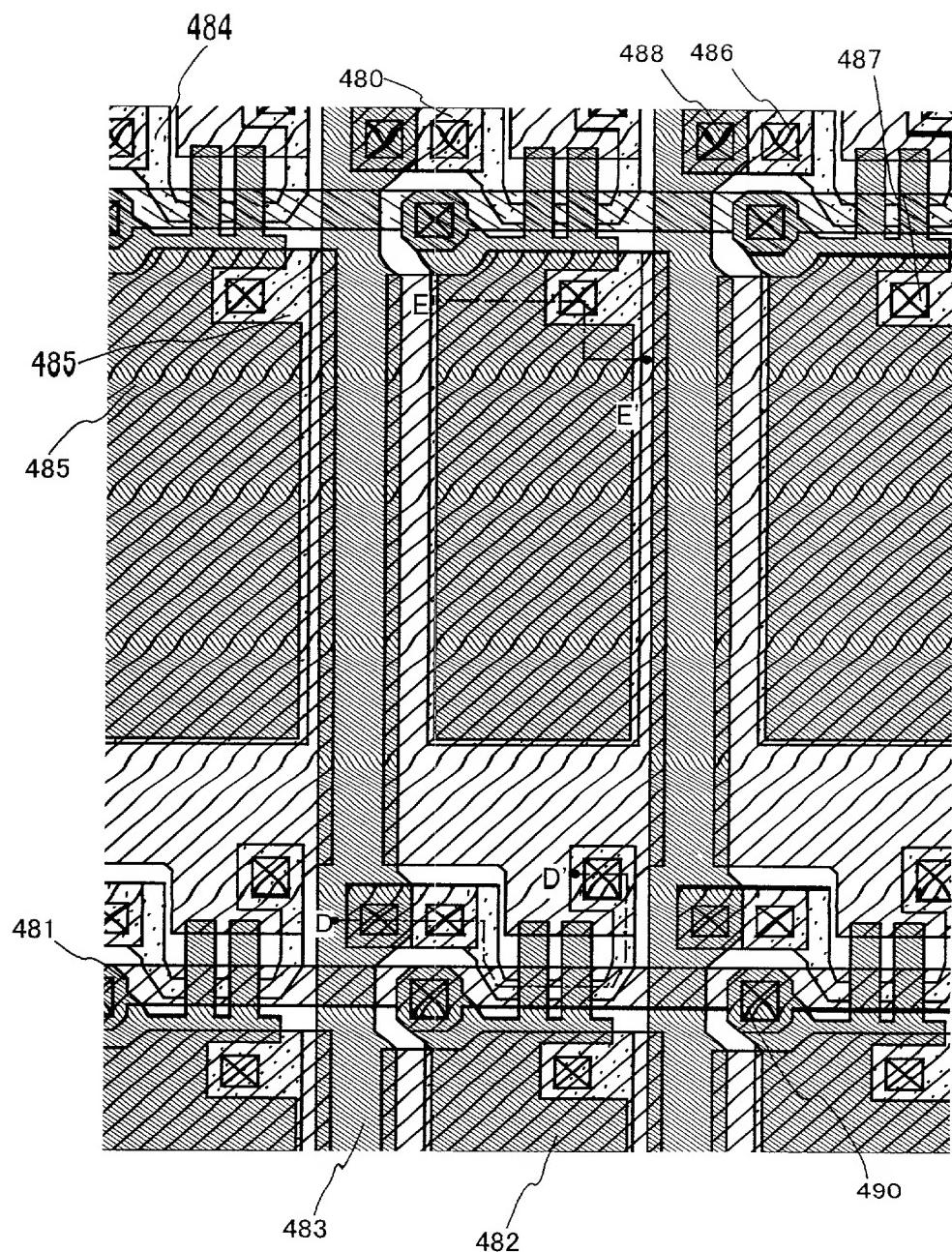


Fig. 12

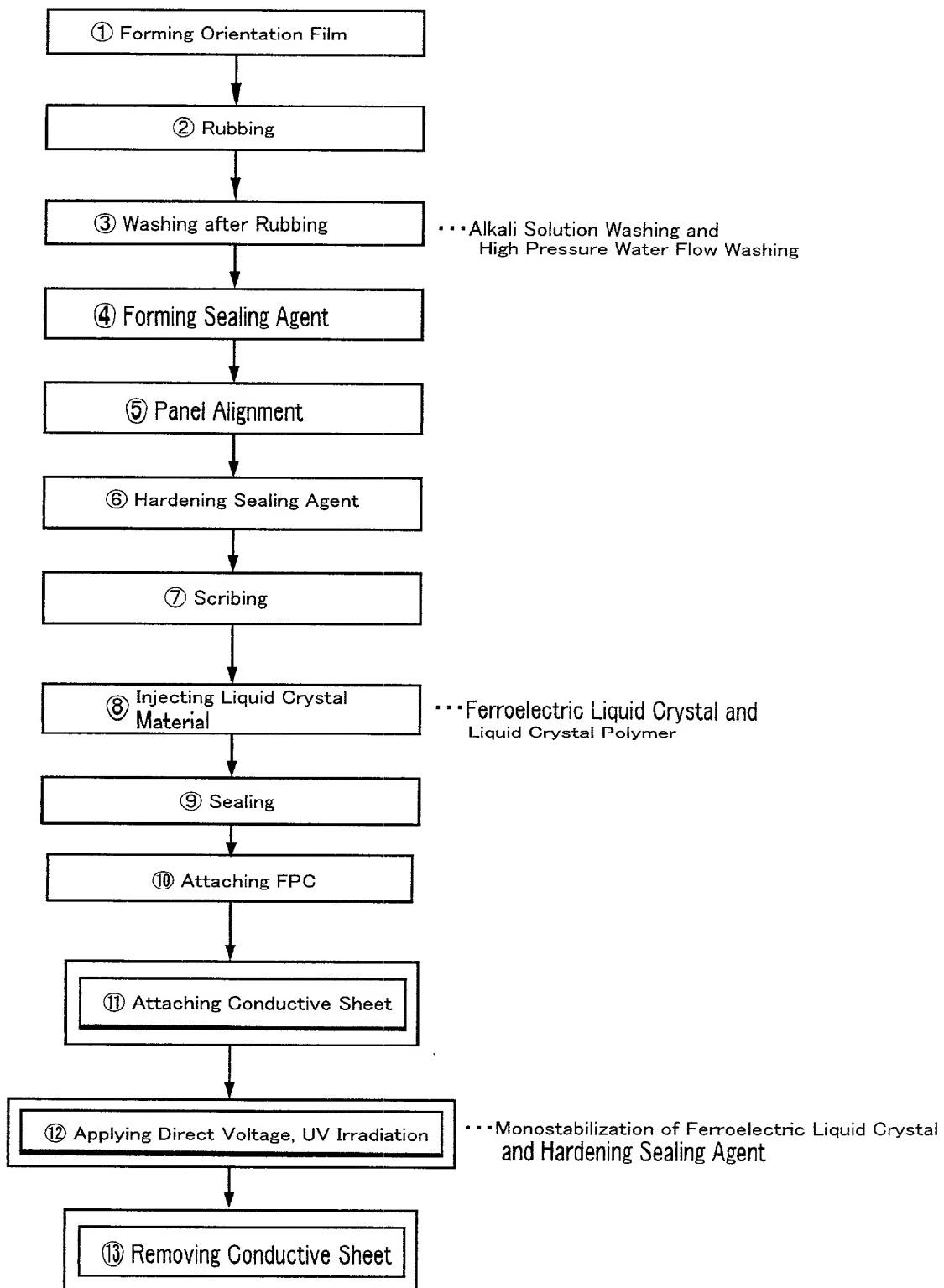


Fig. 13

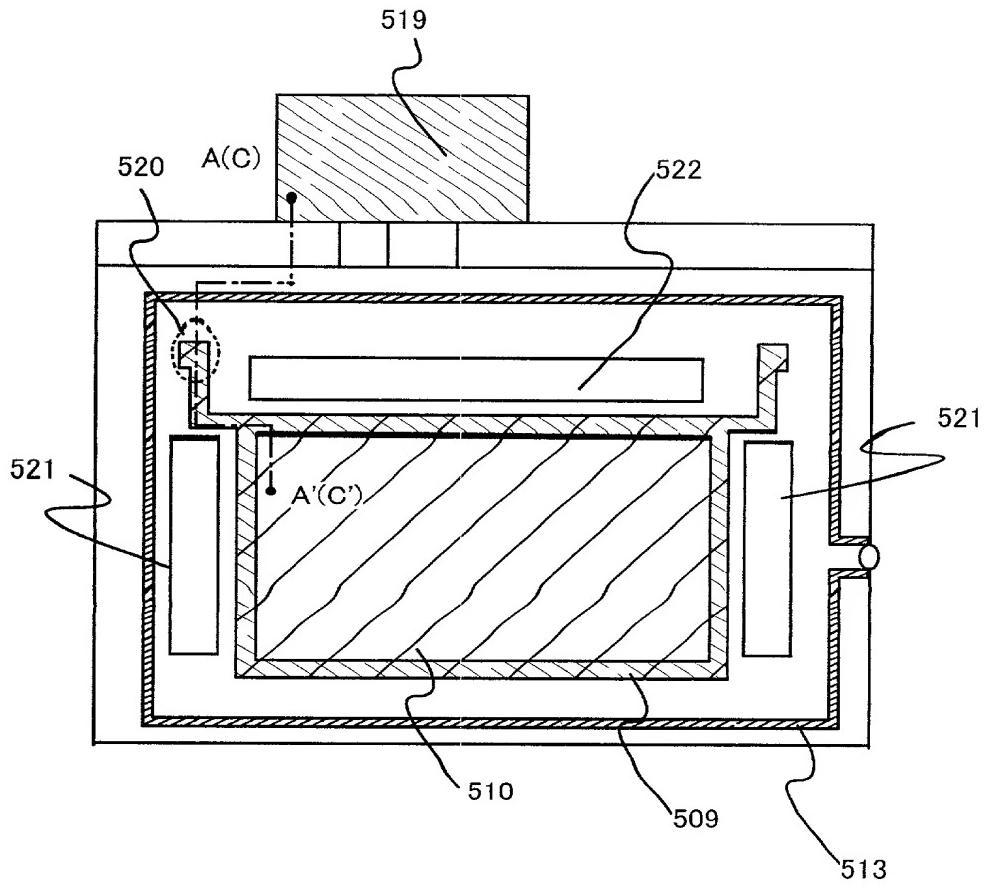
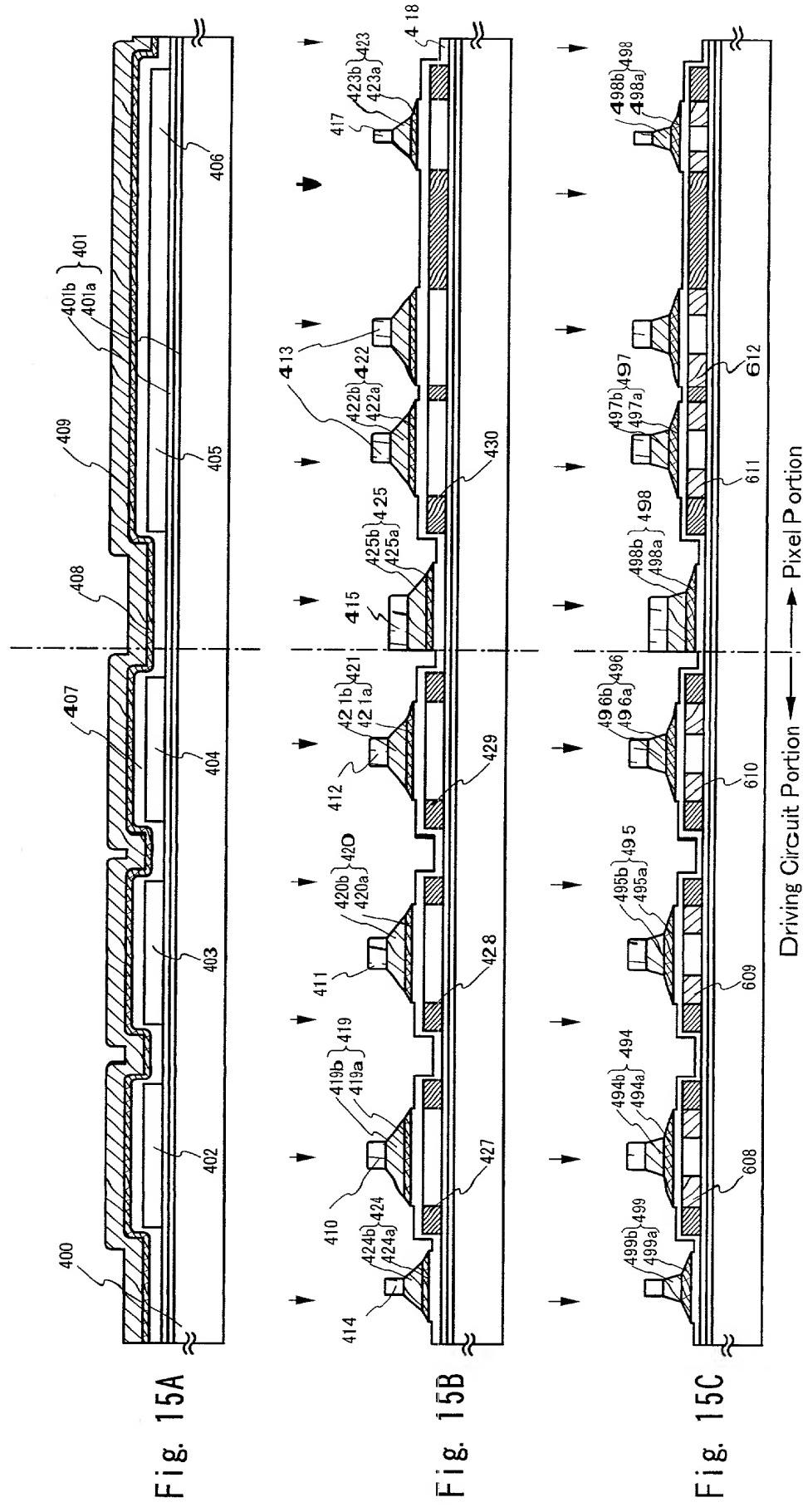
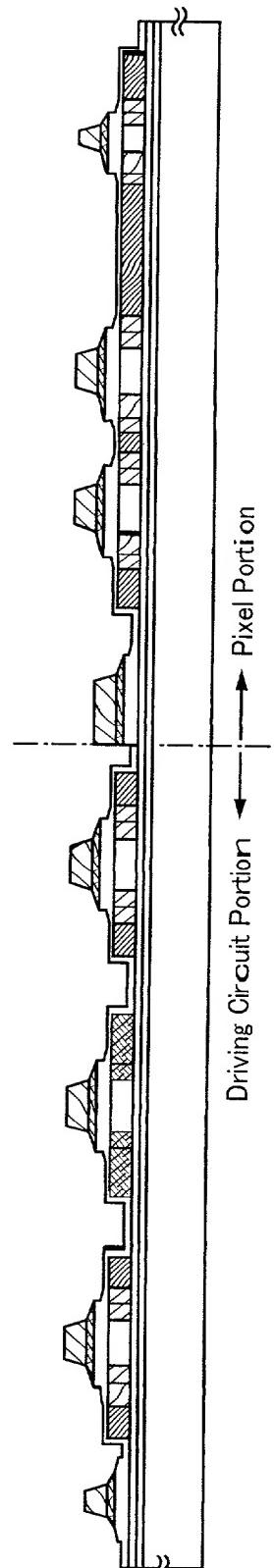
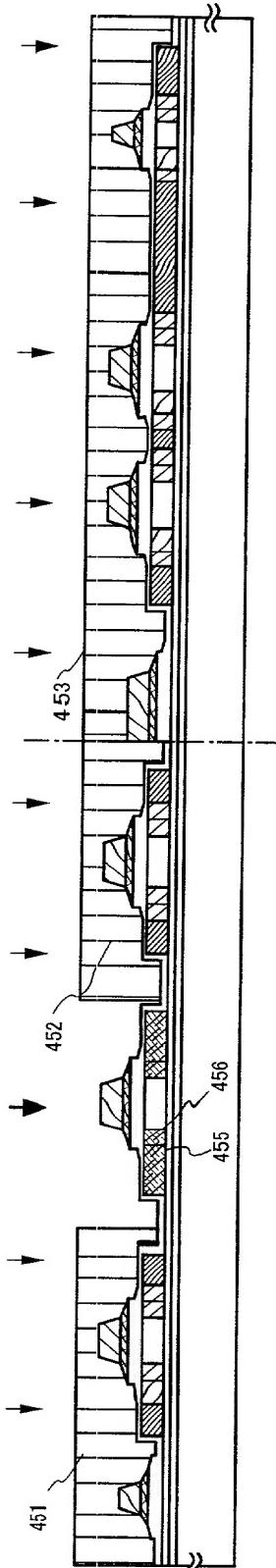
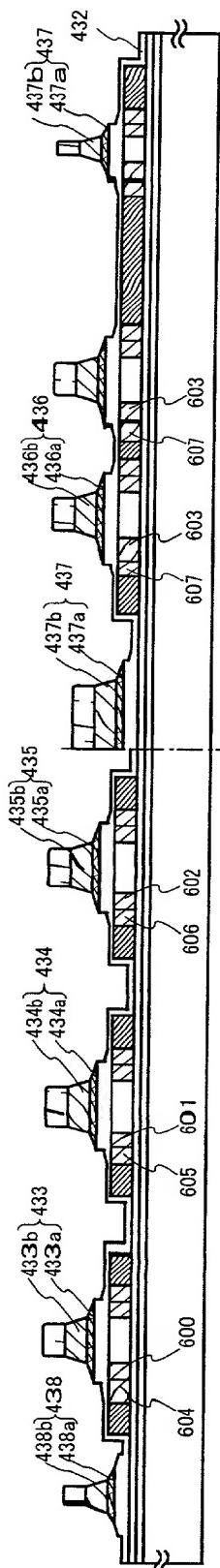


Fig. 14





Driving Circuit Portion → Pixel Portion

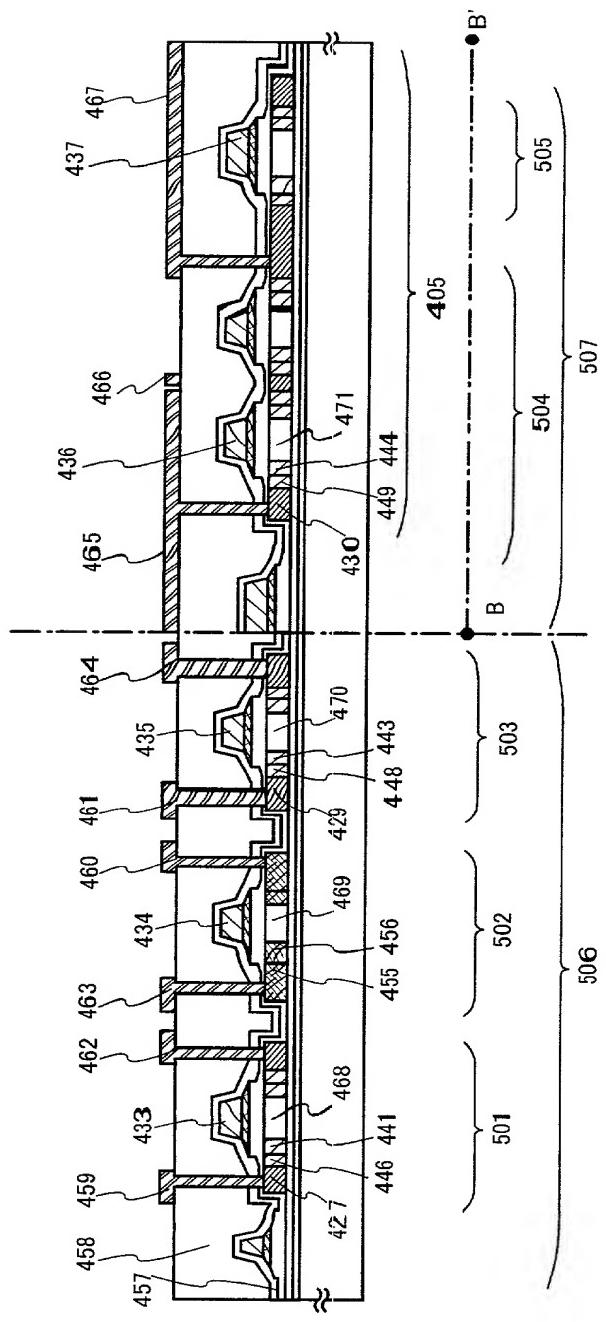
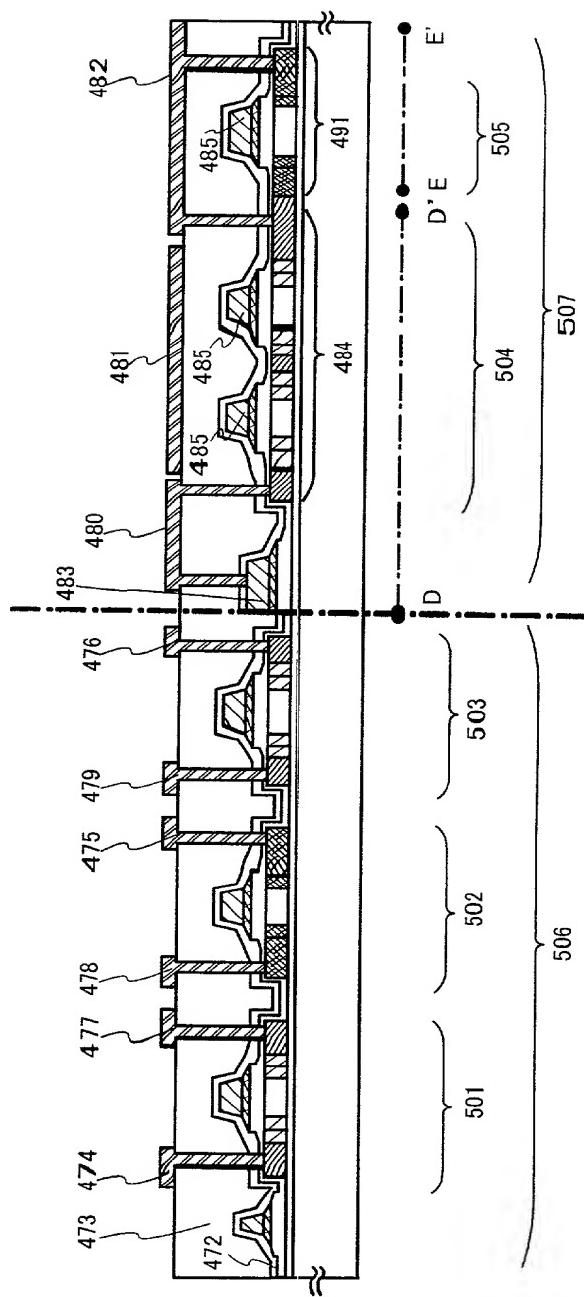


Fig. 17

Fig. 18



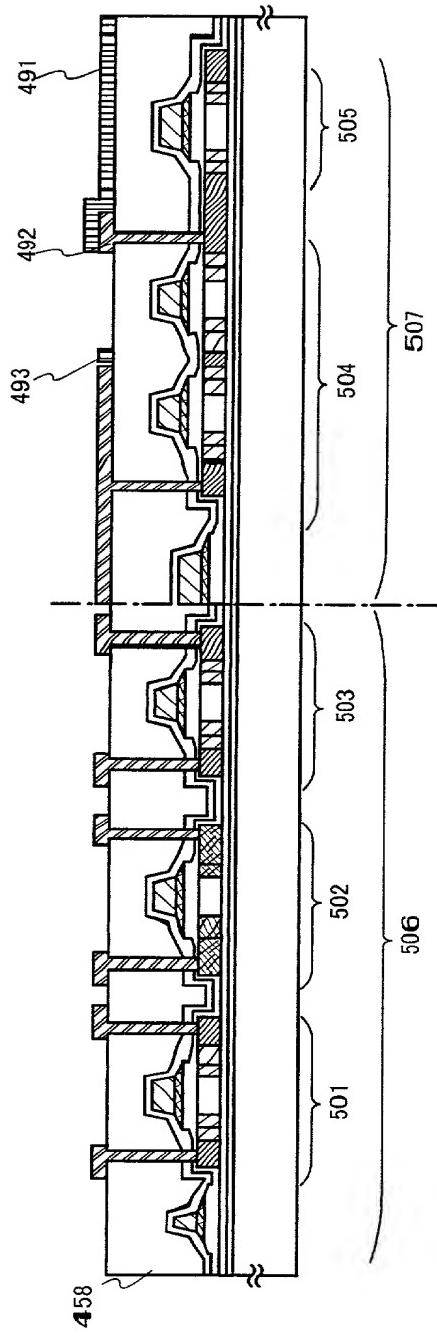


Fig. 19A

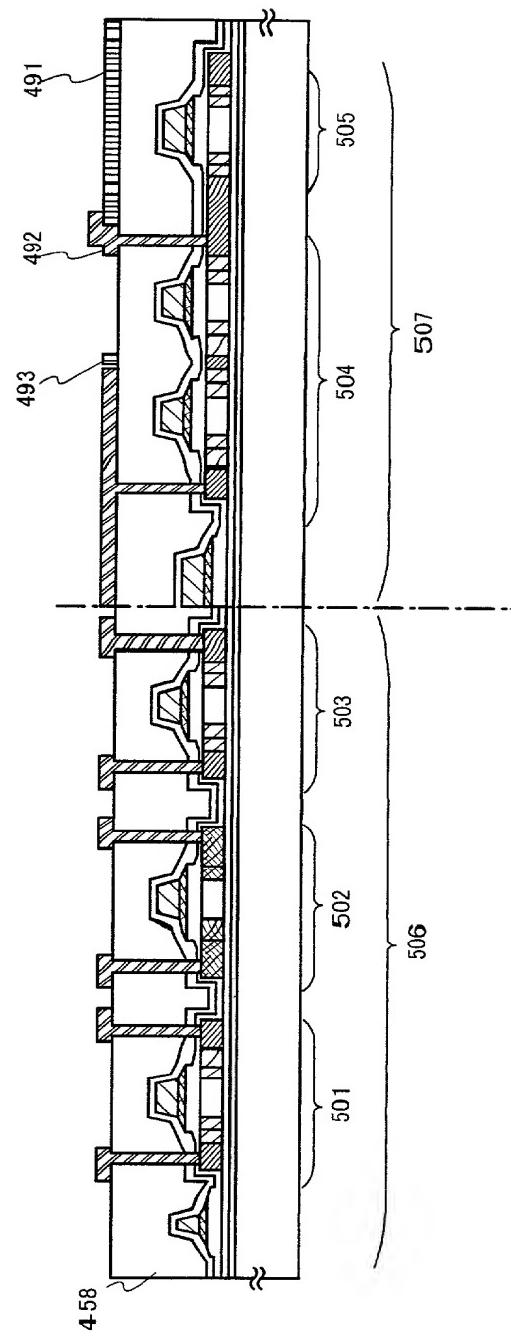


Fig. 19B

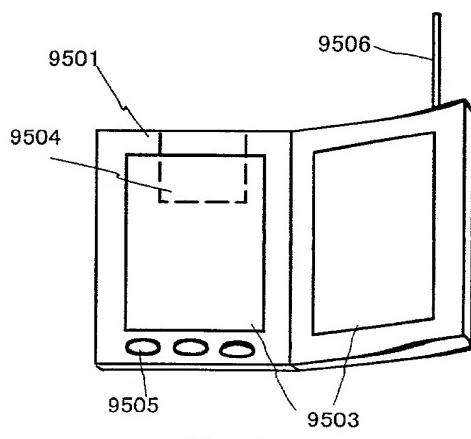
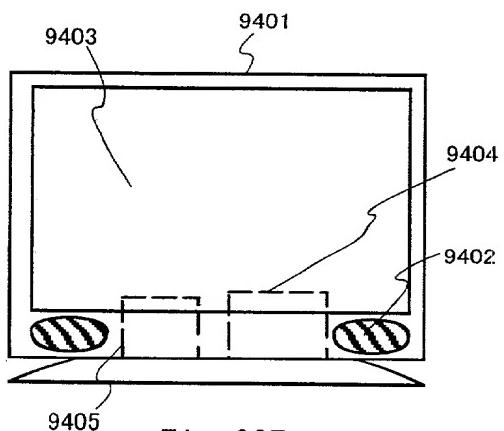
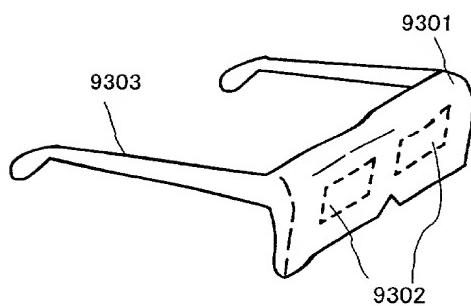
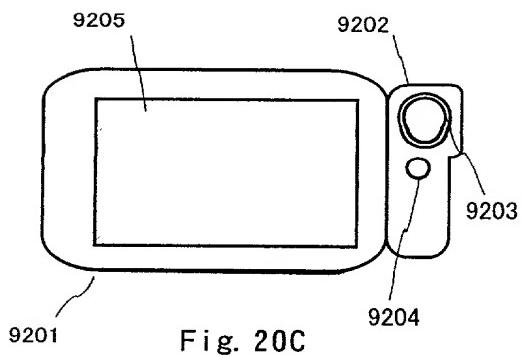
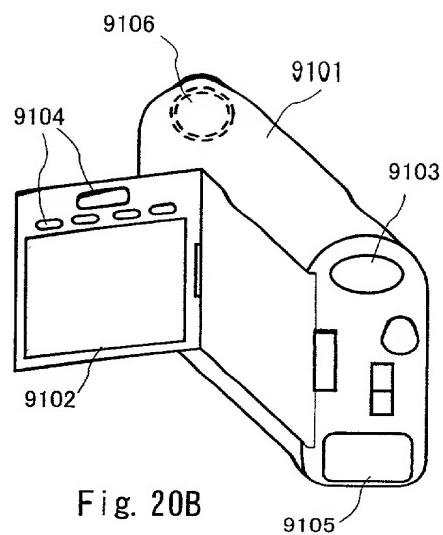
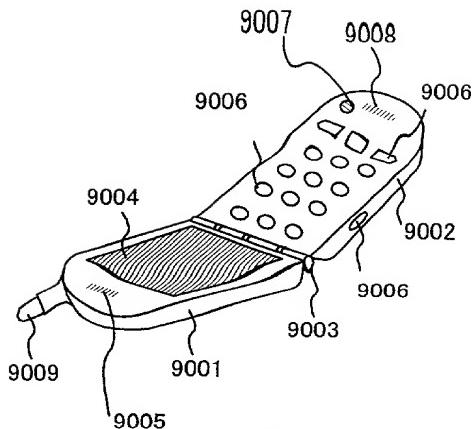


Fig. 21A

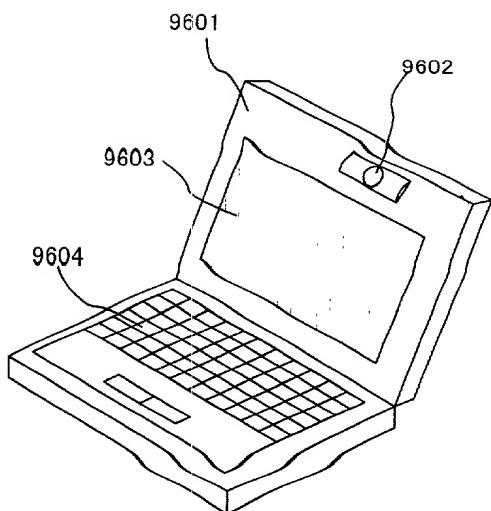


Fig. 21B

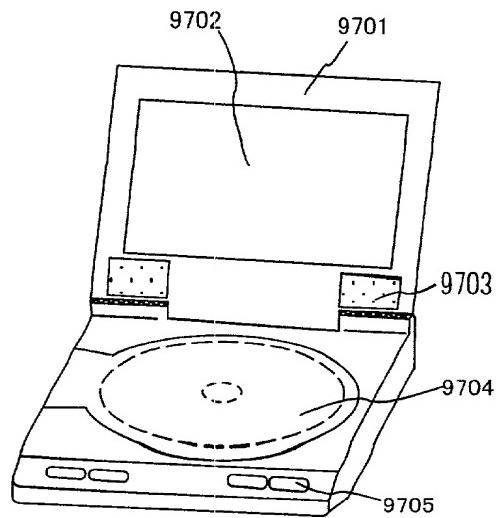
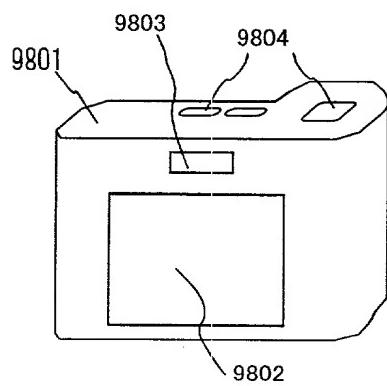


Fig. 21C



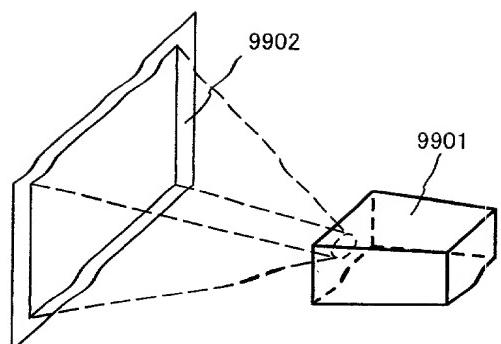


Fig. 22A

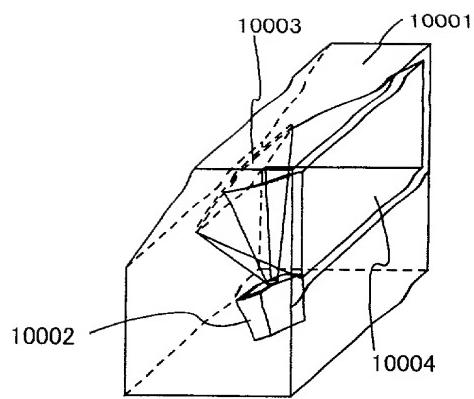


Fig. 22B

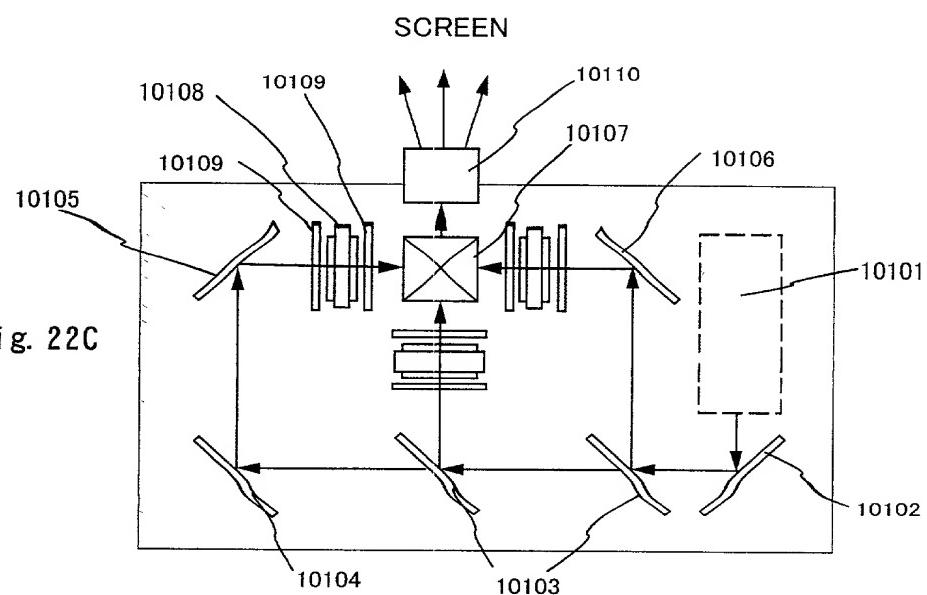


Fig. 22C

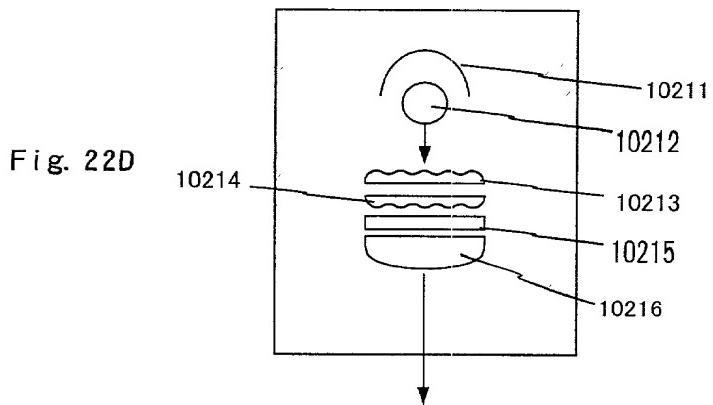


Fig. 22D

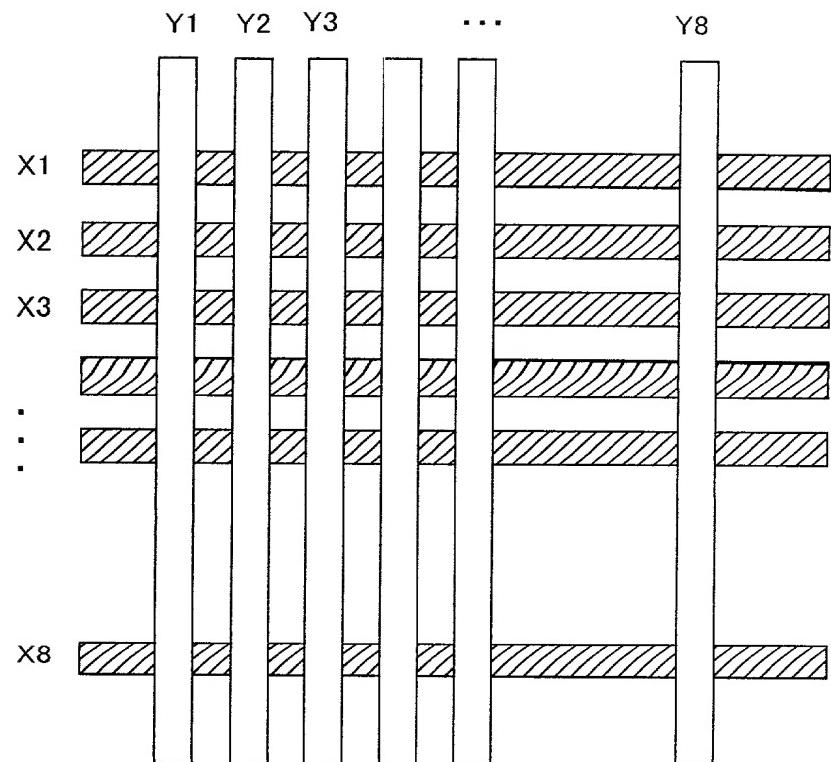


Fig. 23

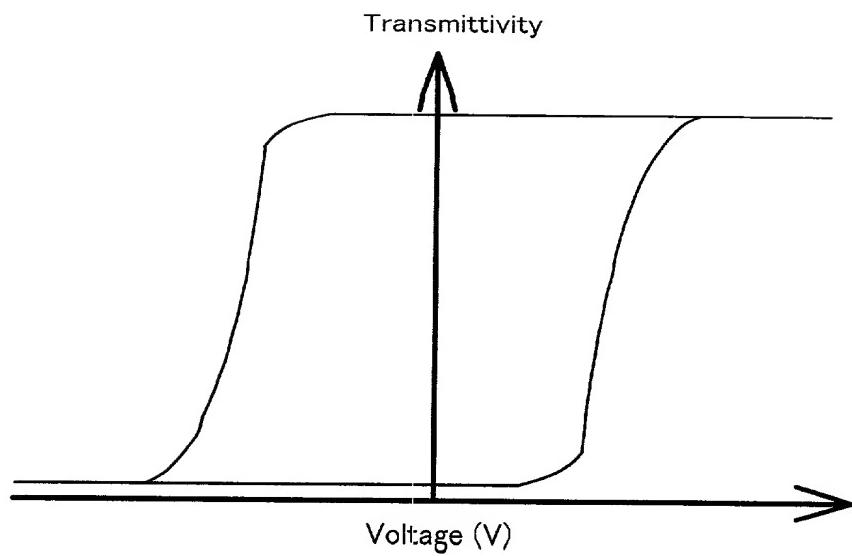


Fig. 24A

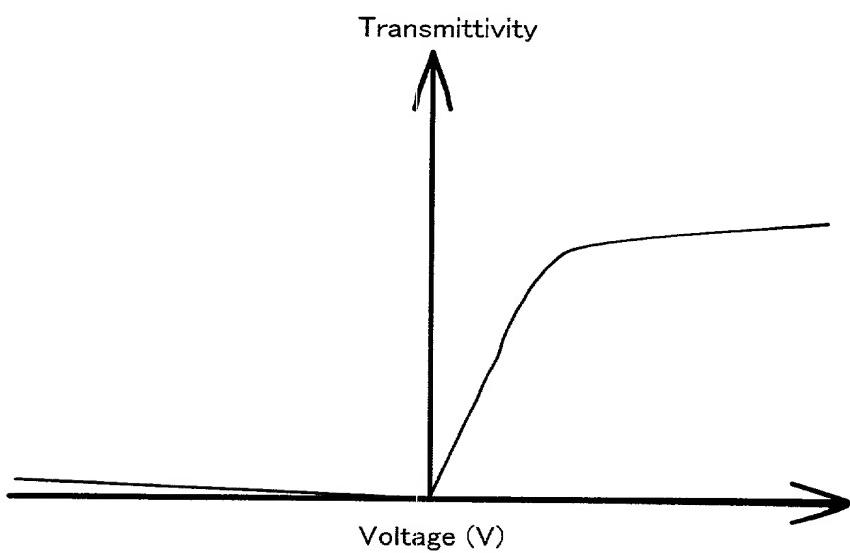


Fig. 24B

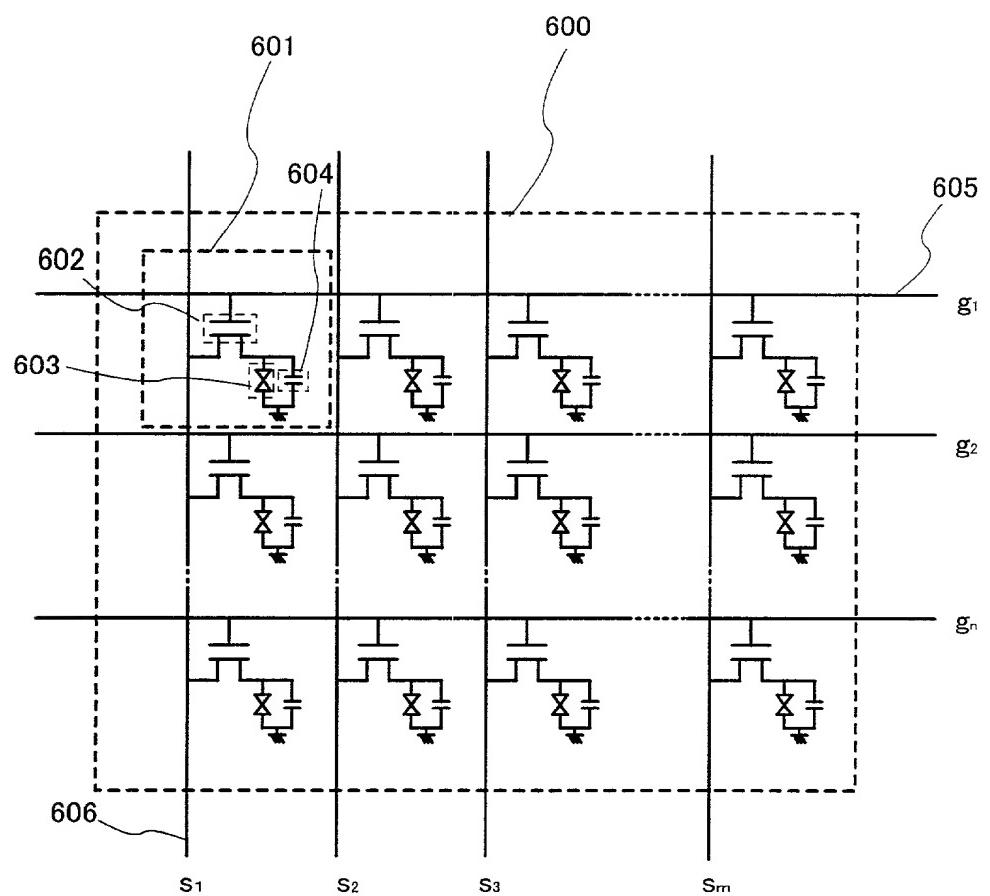


Fig. 25